

Peripheral models for the virtual and RTL Platforms

Internship

GreenWaves Technologies is a fabless semiconductor company based just outside Grenoble, France. We were formed in 2014 with the mission of revolutionizing intelligent sensors and devices. We have developed GAP8, an ultra-low power IoT Application Processor. GAP8 enables devices to interpret and act on image, motion and sound data at energy levels which allow years of operation on batteries bringing low installation and operation costs.

This project aims at providing peripheral models for the virtual and RTL platforms.

Context:

The Gap8 board is a riscv-based microcontroller containing a main core and a cluster of parallel cores for heavy computation. It can be connected to various kind of peripherals using standard interfaces such as SPI, UART and so on.

Several platforms are available for running software on Gap8:

- The physical board
- The virtual platform which is a set of C++ models simulating hardware behavior.
- The RTL platform which is using real hardware simulation.

Responsibilities:

The virtual platform is used to functionally simulate programs using C++ models; the RTL platform is used to do the same using design simulation. Both are connected to C++ models which are modeling peripherals connected to the gap8 chip like cameras, microphones, etc.

The goal of this project is first to extend existing models to cover more device features and to develop new models such as adc, flash, aiming at simulating full applications; the other goal is to extend existing drivers or create new drivers to expose the new peripheral features to the user.

Tasks will include:

- Extending existing models to support more advanced features and extend the driver to expose them to the application developer.
- Creating new models to cover more complex use cases and also add new drivers for them. This can for example be an SPI ADC in order to be able to sample analog signals through the SPI interface.
- Writing tests for the new features and new models to include these new models into the set of automatic tests.

Required skills:

- Proficient C/C++ programming
- notions on systemverilog testbench



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- HW modeling from device specifications

Desired skills:

- Embedded driver development.
- Strong motivation and taste for code optimization (size and speed)

Employment type: Full - time;

Duration : 4 to 6 months;

Location : Villard Bonnot (near Grenoble, France)

Remuneration: 1000 Euros/month + 200 Euros/Month (bonus)

Please send your application to careers@greenwaves-technologies.com